Electronic Acknowledgement Receipt					
EFS ID:	8103306				
Application Number:	10608635				
International Application Number:					
Confirmation Number:	8363				
Title of Invention:	HIGHLY AVAILABLE DUAL SERIAL BUS ARCHITECTURE				
First Named Inventor/Applicant Name:	Bernard Warnakulasooriya				
Customer Number:	47653				
Filer:	Robert Kevin Perkins/Julie Mazzarella				
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1 Change of Address	USPTO_ChangeCorresAdd-723. pdf	188999	no	2	
		c680f41c4f32a65ffa0c58546d92fd4775320 d09			

Warnings:

Information:

2	Change of Address	USPTO_StatmentUnder37cfr37 3-723X.pdf	43194 daec5021283f5fa17a6eb814d1cb446c7831	no	2			
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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

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If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

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